



"state machine" circuit configuration net

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The Transmogrifier C Hardware Description Language and Compiler for FPGAs - group of 7 »

D Galloway - Proceedings of IEEE Workshop on FPGAs for Custom Computing ..., 1995 - doi.ieee.org  
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Petri net synthesis and analysis of a flexible manufacturing systemcell - group of 2 »

M Zhou, K McDermott, PA Patel - Systems, Man and Cybernetics, IEEE Transactions on, 1993 - ieeexplore.ieee.org  
 ... the subnets, for example, all subnets being **state-machine** like nets ... since the physical layout or **configuration** of an ... Since every **circuit** in this subnet has at ...  
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... Configuration of a Run-time Reconfigurable Custom Computing Integrated Circuit for DSP Applications - group of 2 »

MF Cherbaka - 1996 - scholar.lib.vt.edu  
 ... 55 4.6 Crossbar node **state machine** design. ... For example, the clock generation **circuit** was tested extensively for reliability and timing. ... 1.1.2 **Configuration** ...  
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An Open Platform for Development of Network Processing Modules in Reprogrammable Hardware - group of 7 »

JW Lockwood - IEC DesignCon'01 - cs.wustl.edu  
 ... services and com- plex port filtering mechanisms are needed to protect **net-** works from ... RAD Virtual **Circuit** ... Figure 4: Network Interface Device **Configuration** ...  
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Low power pass-transistor logic and application examples - group of 3 »

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 ... 1. The basic system **configuration**. ... language by incor- porating the concept of a **state machine** [6], [17 ... This is a technique that uses relay **circuit** diagrams as a ...  
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